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T-Link-EV5274/5278

User Manual

Rev 0.92

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AMENDMENT HISTORY

| Version | Date | Description |
|---------|-----------|--|
| V0.90 | Oct, 2014 | New release. |
| V0.91 | May, 2015 | 1.修改 P4 文字及圖檔 2.修改 P6 文字 3.修改 P7 文字及圖檔 4.修改 P12 文字及圖檔 5.修改 P14 文字 6.修改 P17 文字 7.新增 P20 文字及圖檔 8.新增 P21 文字及圖檔 9.新增 P22 文字及圖檔 10.新增 P27 文字 11.修改 P29 圖檔 12.修改 P30 圖檔 |
| V0.92 | Sep, 2015 | 1.新增 P26~27 文字及圖檔 |

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1. Outline

Tenx (tenx technology) F51 & L51 Series single-chip is compatible with 8051, the user can use Keil uVision series of software as a development environment, this article will introduce the software and hardware configuration in Keil C.

2. Introduction

Figure 2-1 is T-Link-EV5274/5278 development board, the left portion is called EV5274/5278 Board and the right portion is called T-Link ICE Board to connect PC to use, the user can simulate TM52F5274/74B/78/78B through this development board, please refer to TM52F5274/74B/78/78B datasheet in detail.

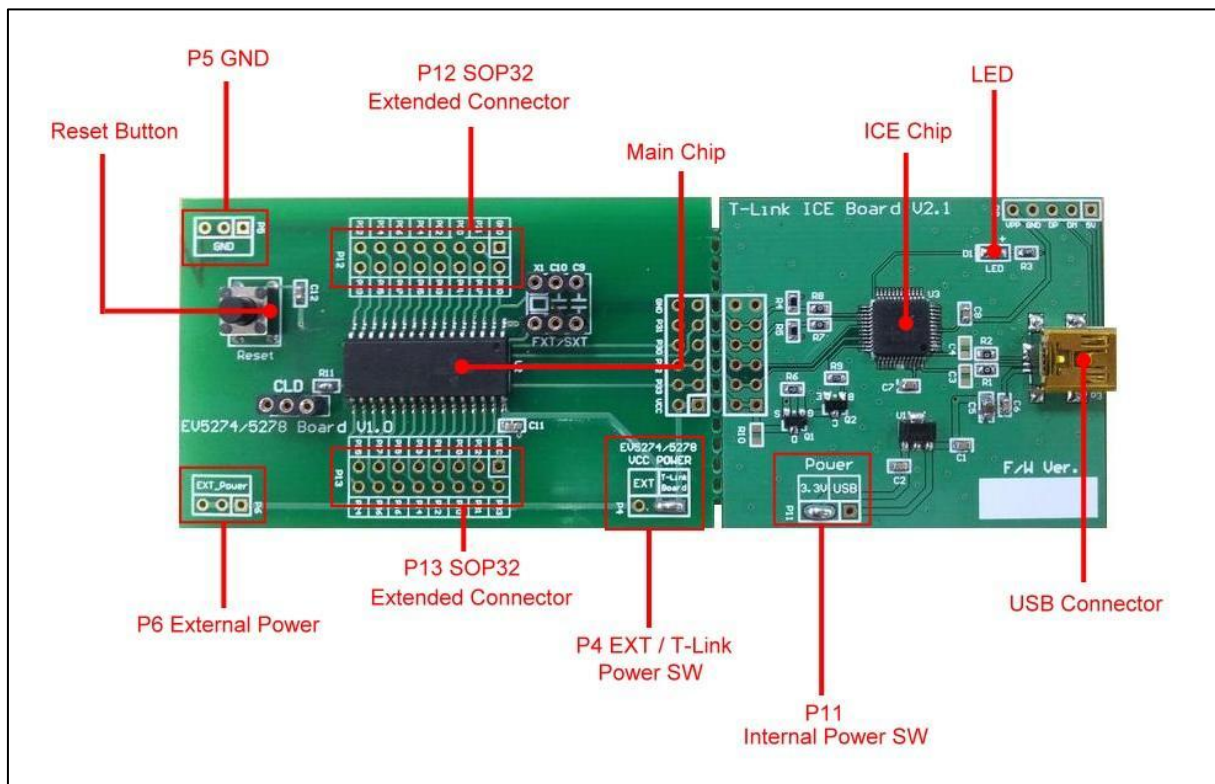
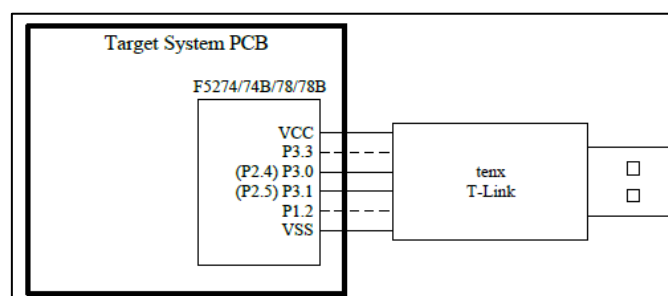


Figure 2-1. T-Link-EV5274/5278 Board



3. T-Link-EV5274/5278 Hardware Description

3.1 Power Setting

- P6: Connect to external power (EV5274/5278 Board)
- P4: Internal or external power source selection (EV5274/5278 Board)
- P11: Internal power selection (T-Link Board)

| MCU Power | P11 | P6 | P4 |
|----------------|-------------|-----------|--------------|
| External Power | USB or 3.3V | EXT_Power | EXT |
| Internal Power | USB or 3.3V | X | T-Link Board |

3.2 USB Connector

- P3: Mini USB connector to PC (T-Link Board)

3.3 Reset Button

- Reset: Reset Button (EV5274/5278 Board)

3.4 External Power Connector

- P6: External VCC connector (EV5274/5278 Board)
- P5: External GND connector (EV5274/5278 Board)

3.5 MCU External Frequency-FXT/SXT

- X1: crystal FXT(1~8M) / SXT(32.768K) (EV5274/5278 Board)
- C9: matching capacitor (EV5274/5278 Board)
- C10: matching capacitor (EV5274/5278 Board)

3.6 PIN Assignment & Description

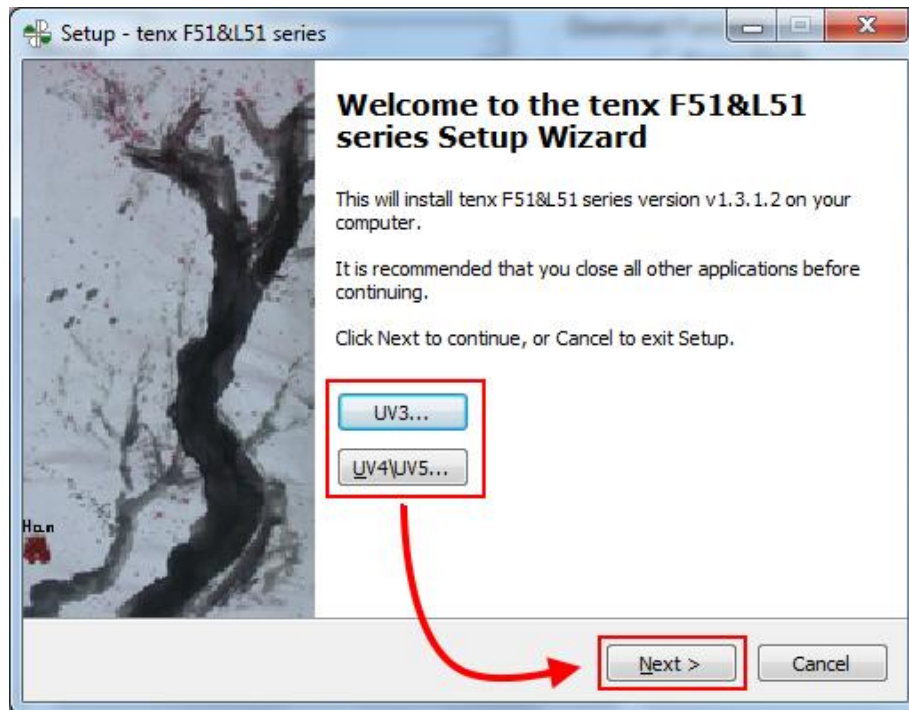
EV5274/5278 Board contains TM52F5274/74B/78/78B main chip, packaged is SOP 32-pin as shown below table for the pin assignment instructions.

| Pin Number | Pin Name | Pin Number | Pin Name |
|------------|--------------------------------------|------------|--------------------------|
| 1 | VSS | 17 | P2.4 |
| 2 | XI/P2.0 | 18 | P2.5 |
| 3 | XO/P2.1 | 19 | TK11/SEG6/SCK/P3.6 |
| 4 | VPP/RSTn/INT2/P3.7 | 20 | TK10/SEG7/MISO/P1.7 |
| 5 | COM3/P0.0 | 21 | TK9/SEG8/P1.6 |
| 6 | COM2/P0.1 | 22 | CLD/SEG9/AD9/P1.5 |
| 7 | COM1/P0.2 | 23 | TK8/SEG10/AD8/CKO/P1.4 |
| 8 | COM0/P0.3 | 24 | TK3/SEG11/AD7/PWM1A/P1.3 |
| 9 | SEG0/P0.4 | 25 | TK2/SEG12/AD6/PWM0A/P1.2 |
| 10 | SEG1/P0.5 | 26 | TK1/SEG13/AD5/T2EX/P1.1 |
| 11 | SEG2/P0.6 | 27 | TK0/SEG14/AD4/T2/P1.0 |
| 12 | SEG3/P0.7 | 28 | TK7/SEG15/AD3/RXD/P3.0 |
| 13 | TK13/ \overline{SS} /SEG4/T0/ P3.4 | 29 | TK6/SEG16/AD2/TXD/P3.1 |
| 14 | TK12/MOSI/SEG5/T1/ P3.5 | 30 | TK5/SEG17/AD1/INT0/P3.2 |
| 15 | PWM0B/P2.2 | 31 | TK4/AD0/INT1/P3.3 |
| 16 | PWM1B/P2.3 | 32 | VCC |

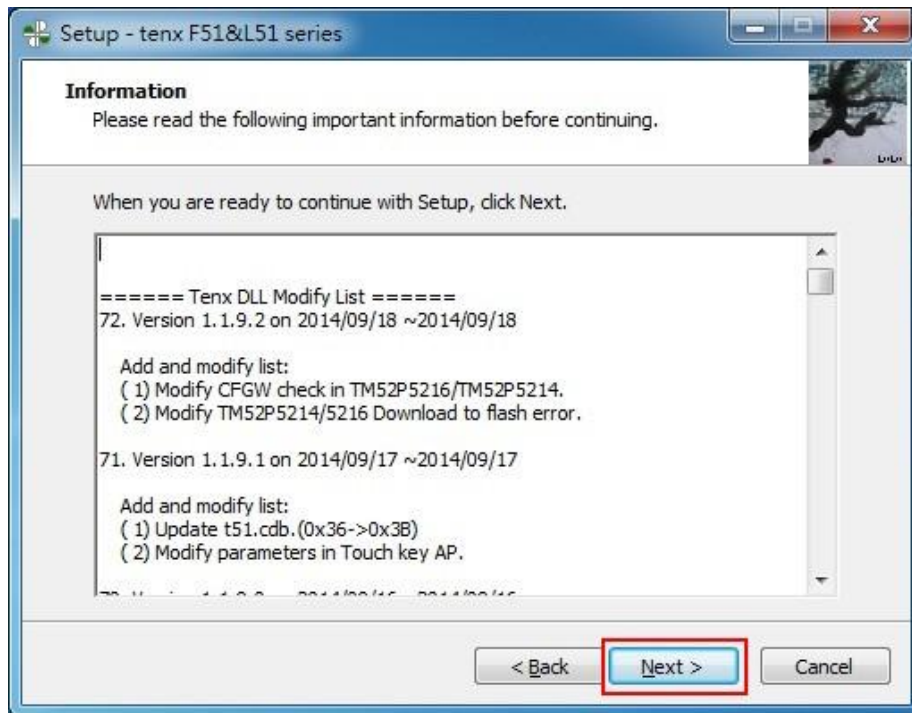
4. How to start using T-Link-EV5274/5278 simulation

First, Install the tenx TM52D11 IDE file to follow steps to complete the installation, the IDE & Keil C version must be installed in the same path, the default path is C:\ Keil:

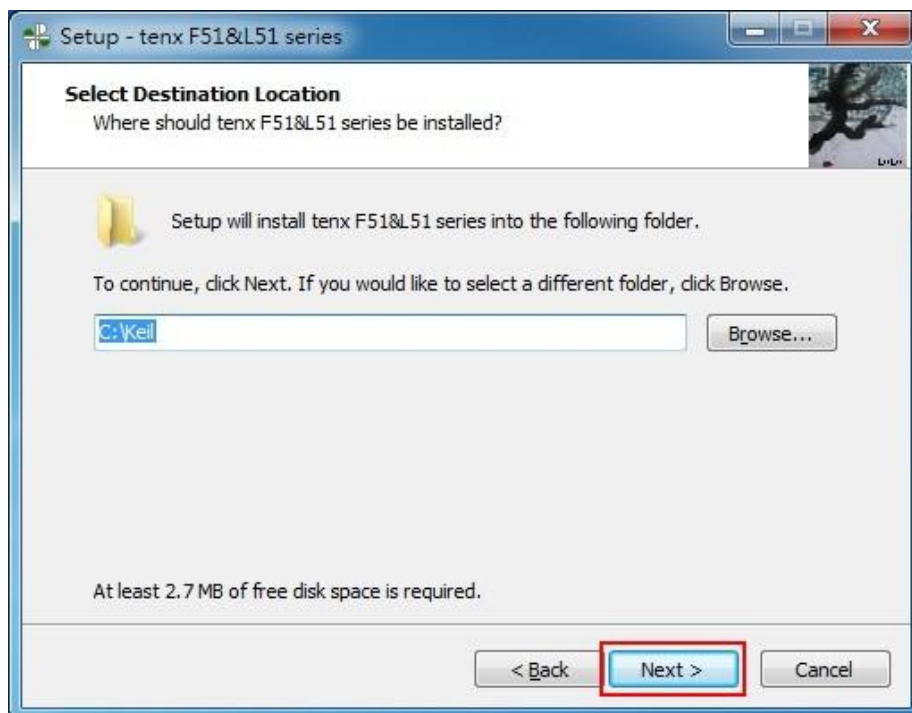
4.1 Select Keil C version, click “Next>”



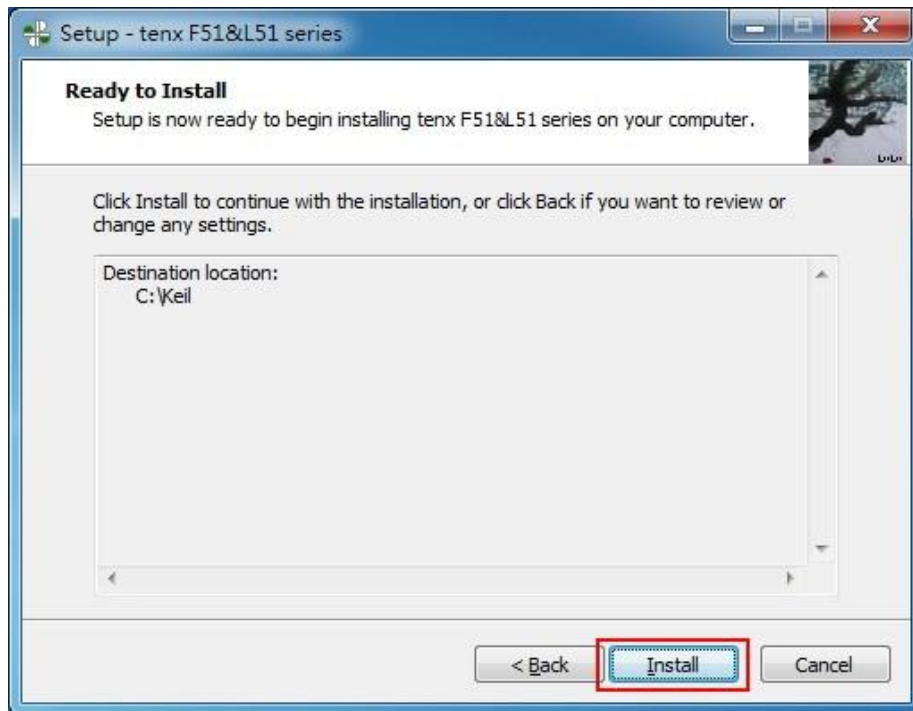
4.2 And then click “Next>”



4.3 The default path is C:\ Keil, click “Next>”



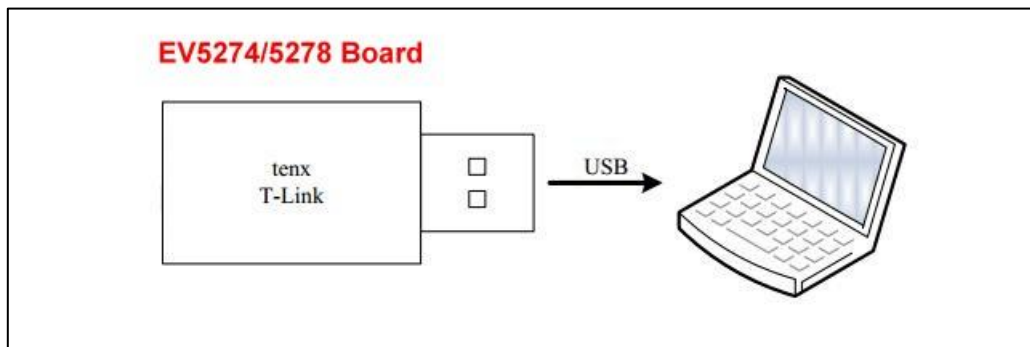
4.4 Click “Install”



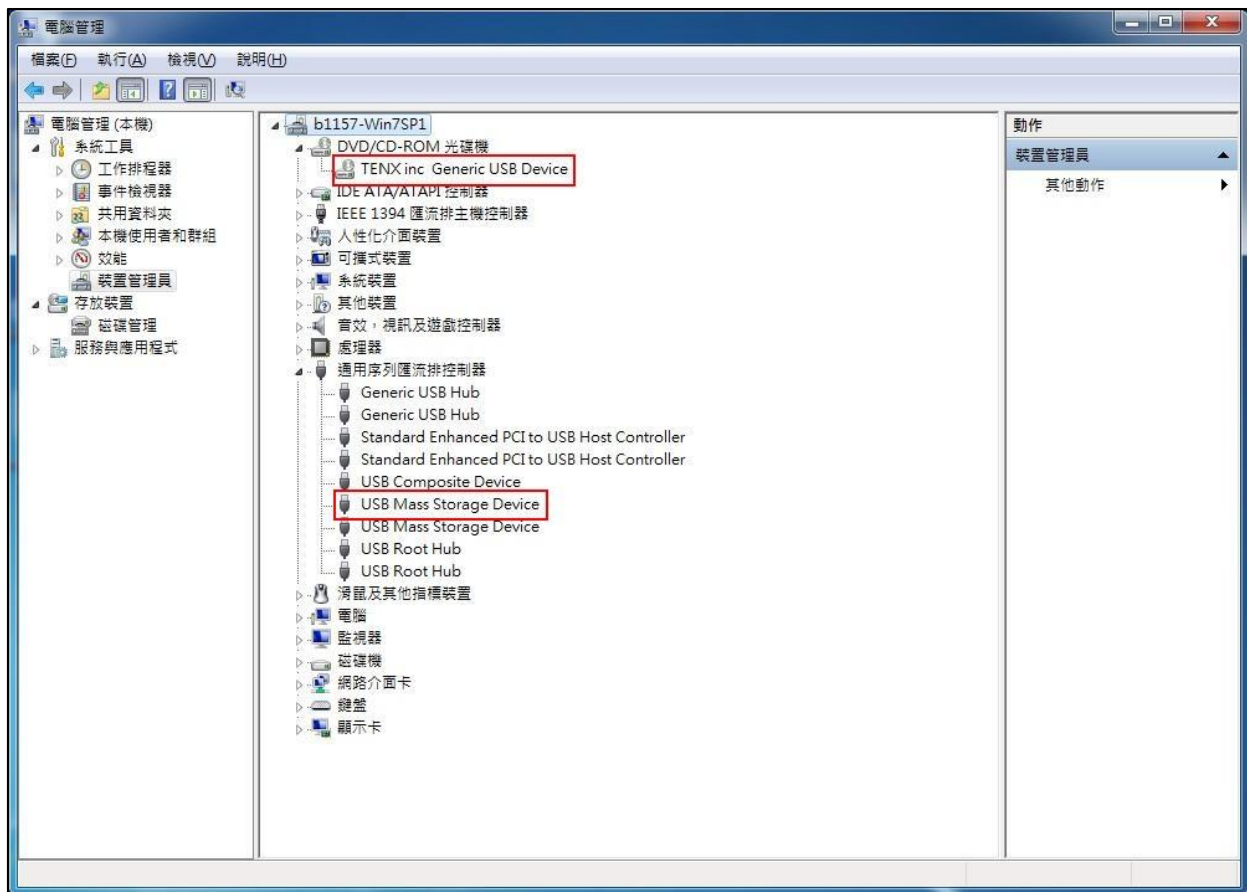
4.5 Click “Finish” to complete the installation



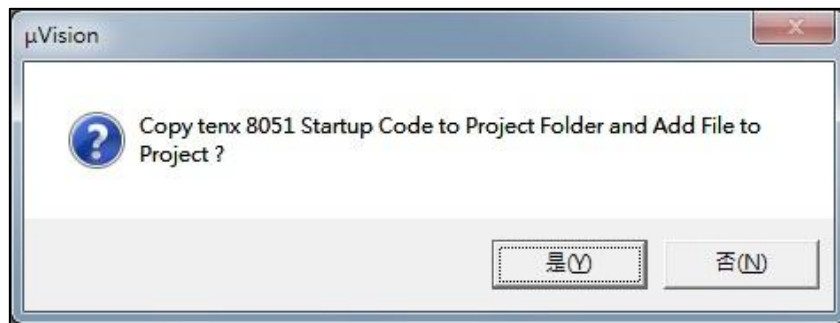
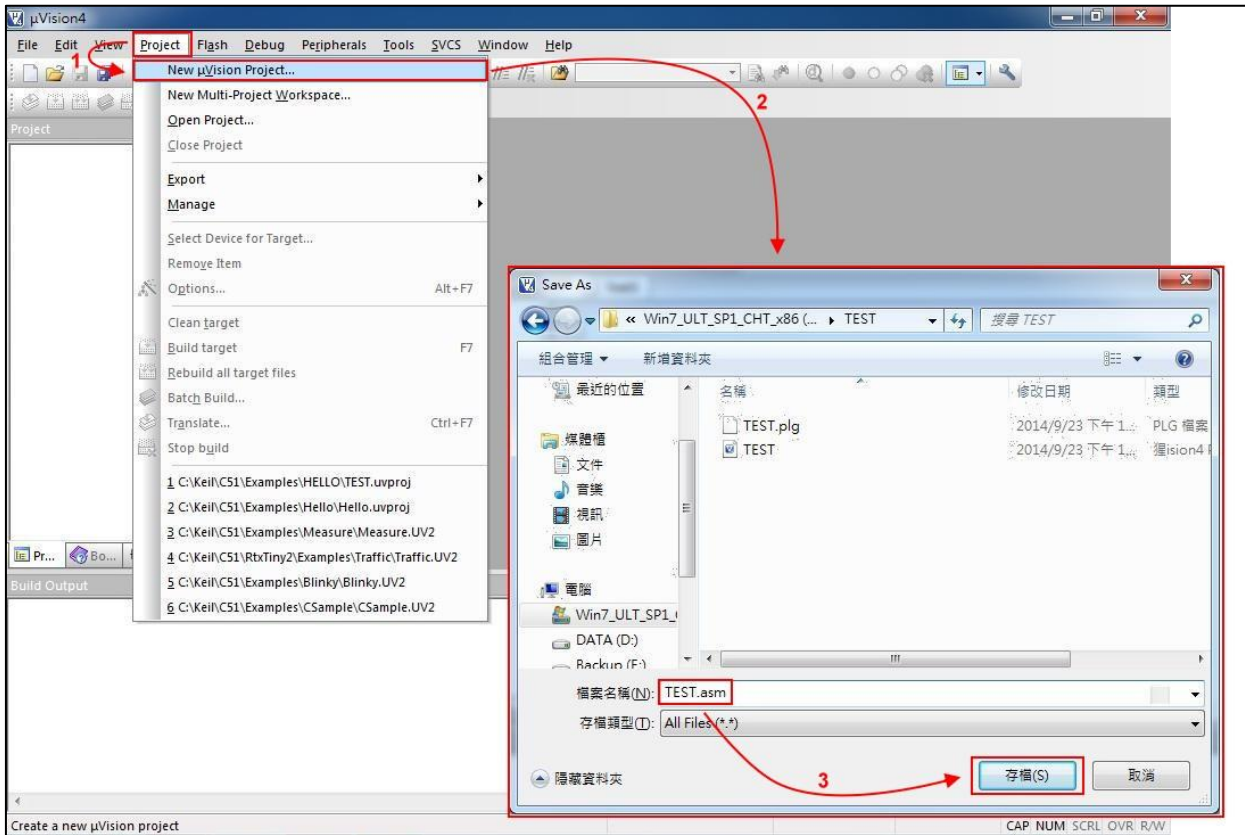
4.6 Connect T-Link-EV5274/5278 Board of USB (mini type) connector to PC



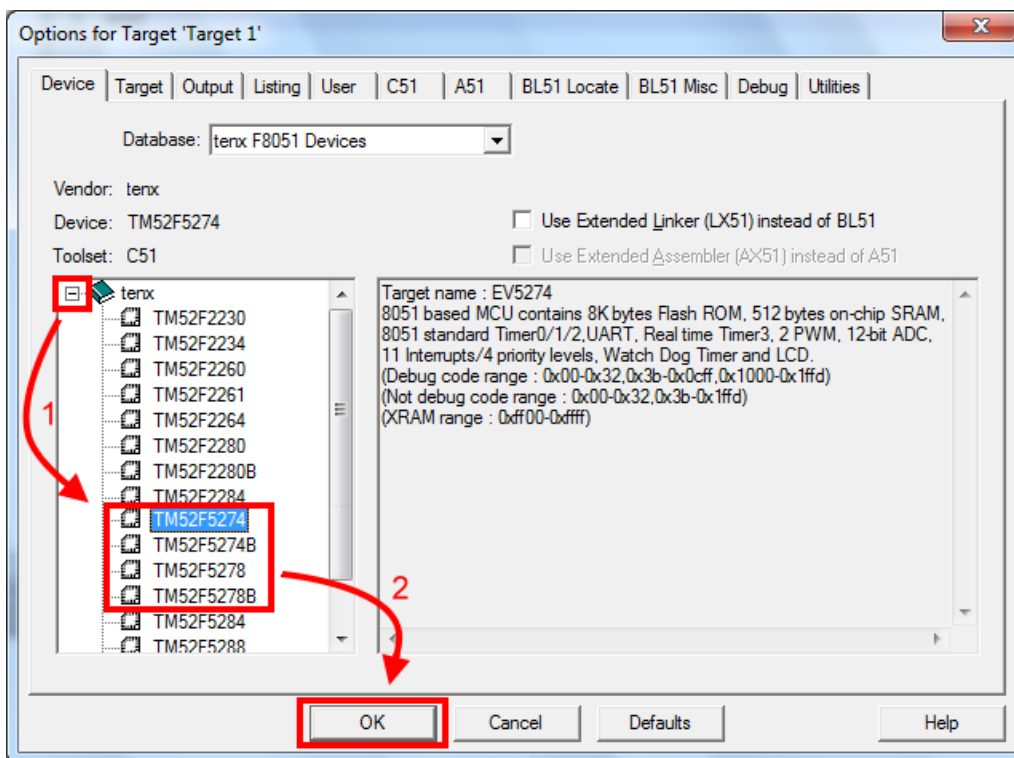
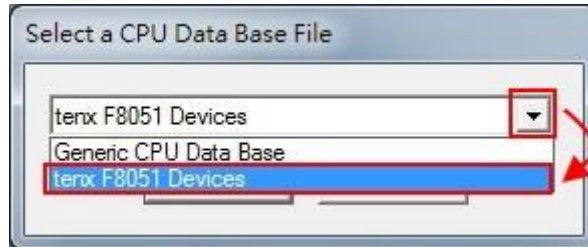
4.7 Confirm Device Manger



4.8 Open the KEIL C → Click the Project menu and select New Project window → Fill in the project name and click Save Project → Copy tenx 8051 Startup Code or not ?



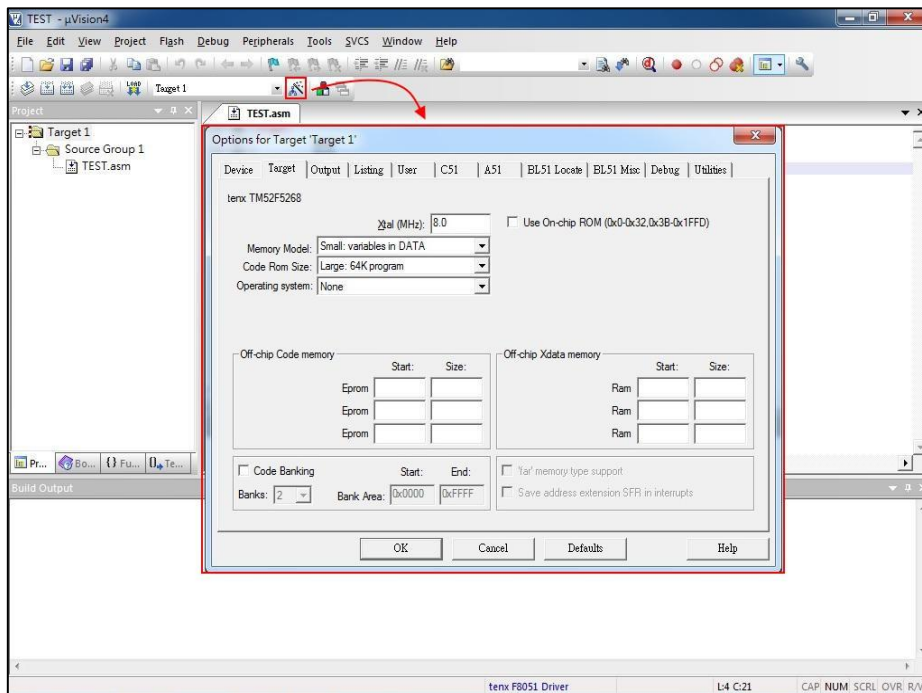
4.9 CPU Data Base selection dialog box will be show up → Select the tenx F8051 Devices and click on OK button → Select a CPU model (for EV Board model), refer to Note (1) and the right window shows some of the parameters of this single chip model.



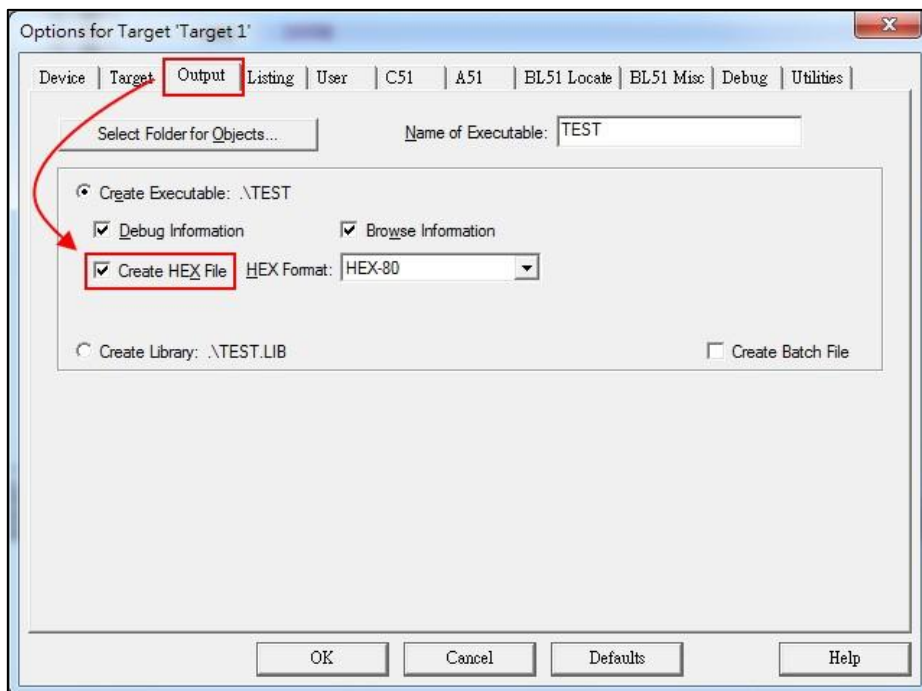
Note (1): TM52F5274/74B/78/78B (For EV Board model)

| Chip Model | EV Board Model |
|---------------|----------------|
| TM52F5274/74B | EV5274/5278 |
| TM52F5278/78B | |

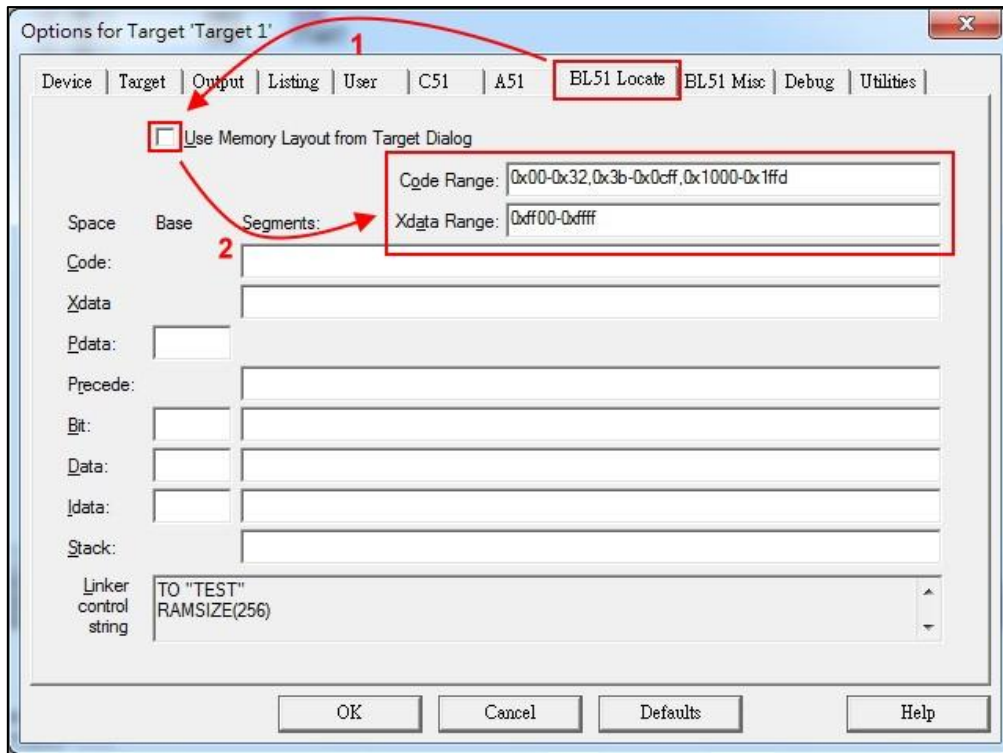
4.10 Option for Target dialog box settings: Click the “Option for Target” button in the main menu, the project file is created must also be relevant to the project file settings as shown below.





4.11 Output dialog box settings: To confirm “Create HEX File” option is checked, as shown below.



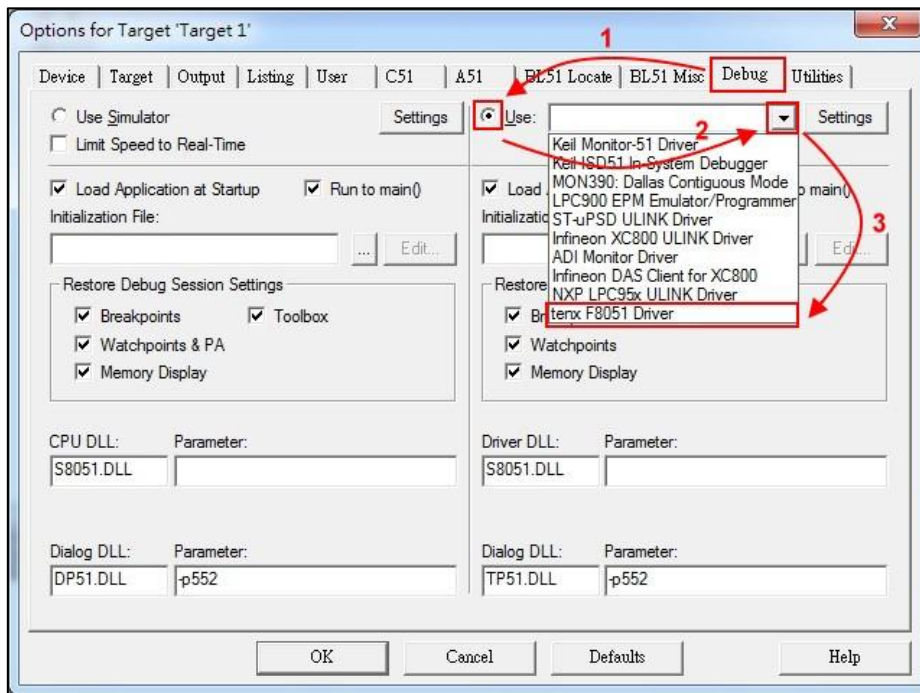
4.12 BL51 Locate dialog box settings: Unchecked “Use Memory Layout from Target Dialog” in BL51 Locate option and fill in Code & Xdata Range (Note: The chip code available range has divided into download mode and debug mode, please refer to Note (2))



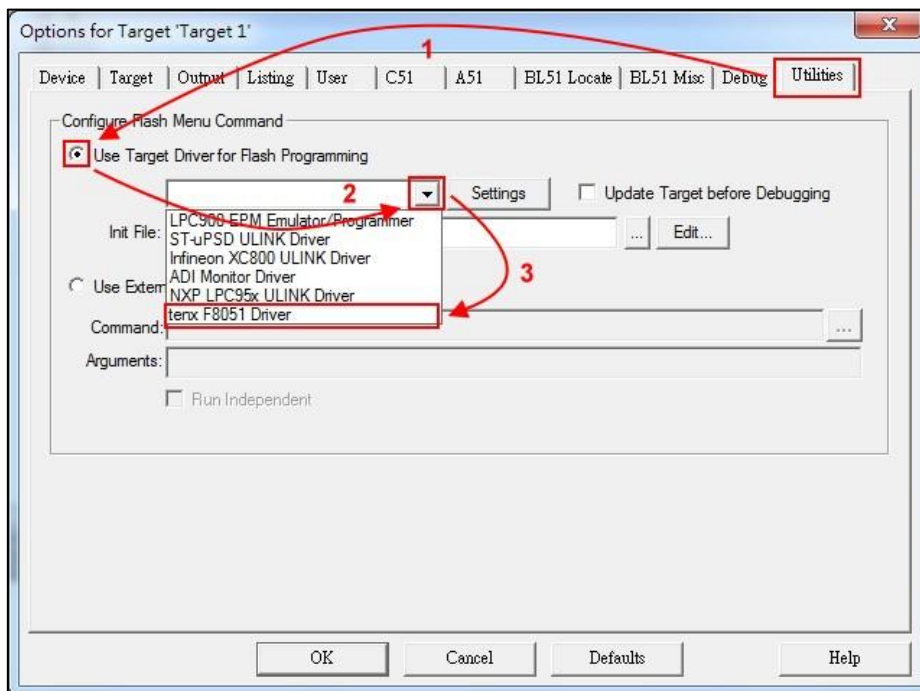
Note (2): TM52F5274/74B/78/78B (EV5274/5278) Code Range area (Program ROM)

| Chip Model | Code Range | | XRAM Range |
|---------------------------------------|---|--|---------------|
| | Download Mode  | Debug Mode  | |
| TM52F5274/74B/78/78B (EV5274/5278) | 0x00-0x32, 0x3b-0x1ffd | 0x00-0x32, 0x3b-0x0cff, 0x1000-0x1ffd | 0xff00-0xffff |

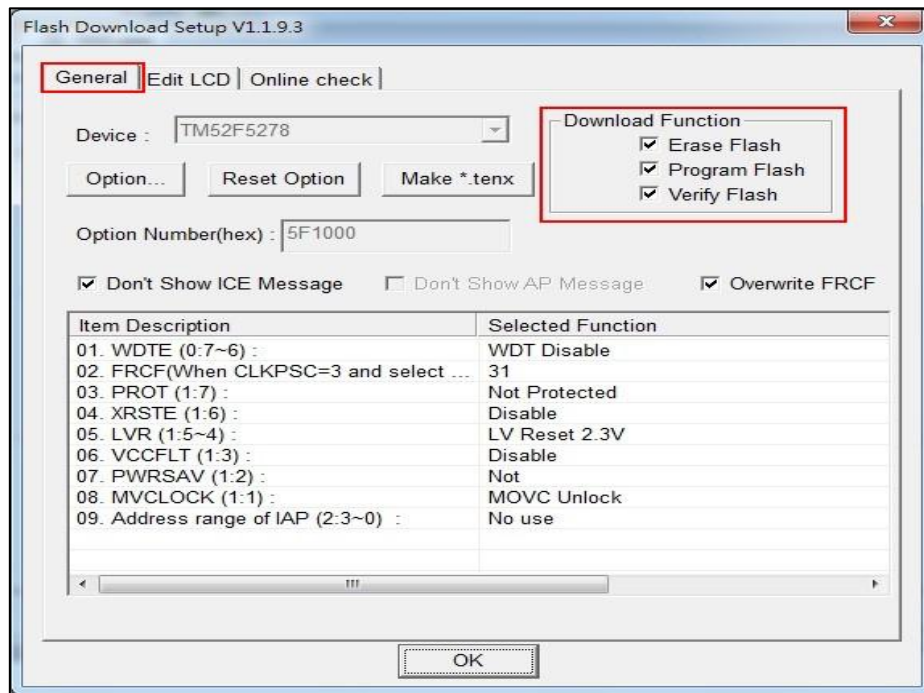
4.13 Debug dialog box settings: Click on “Use:” option, and then select “tenx F8051 Driver” as shown below.



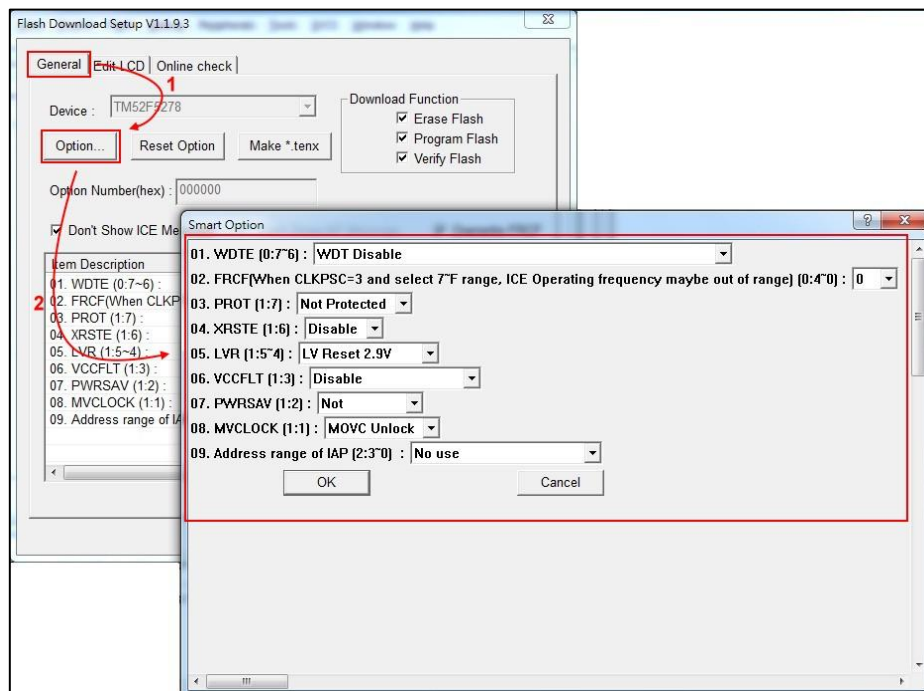
4.14 Utilities dialog box settings: Click on “Use Target Driver for Flash Programming” option and select “tenx F8051 Driver” as shown below.



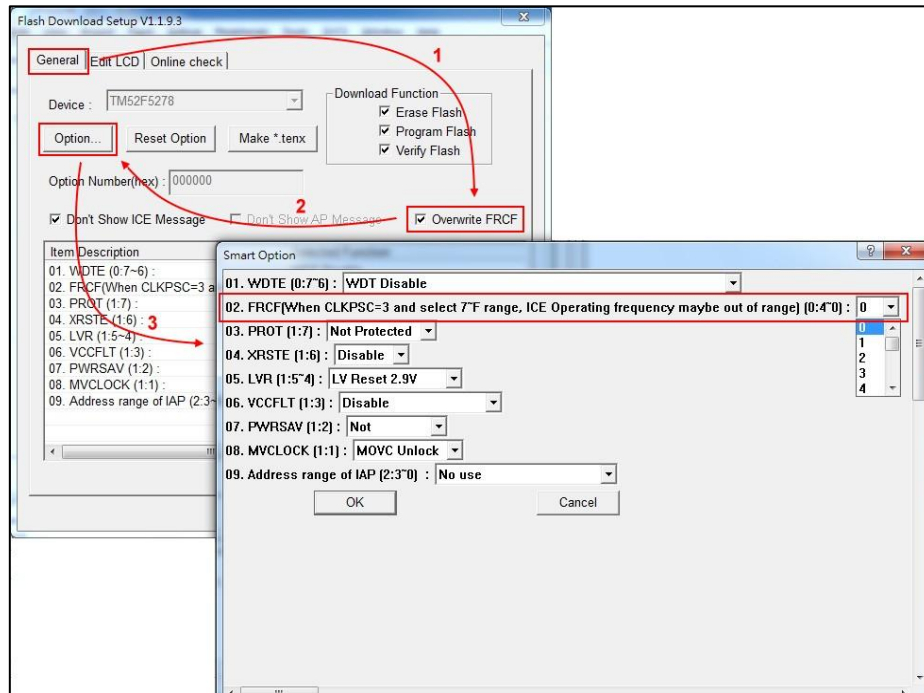
4.15 Click on “settings” button and the “Flash Download Setup” window will be show up, check the “Download Function” desired option as shown below.



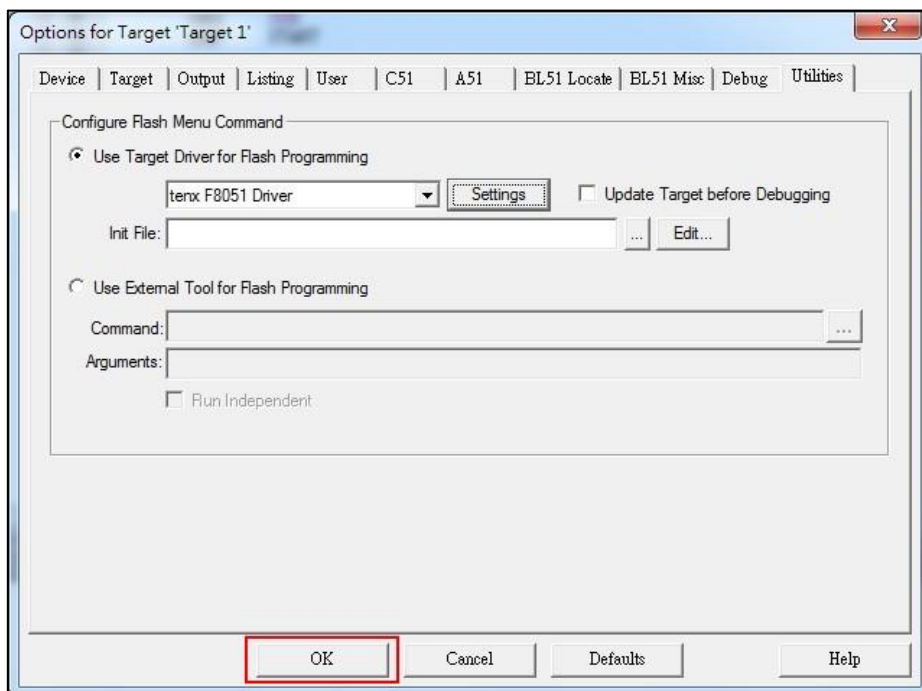
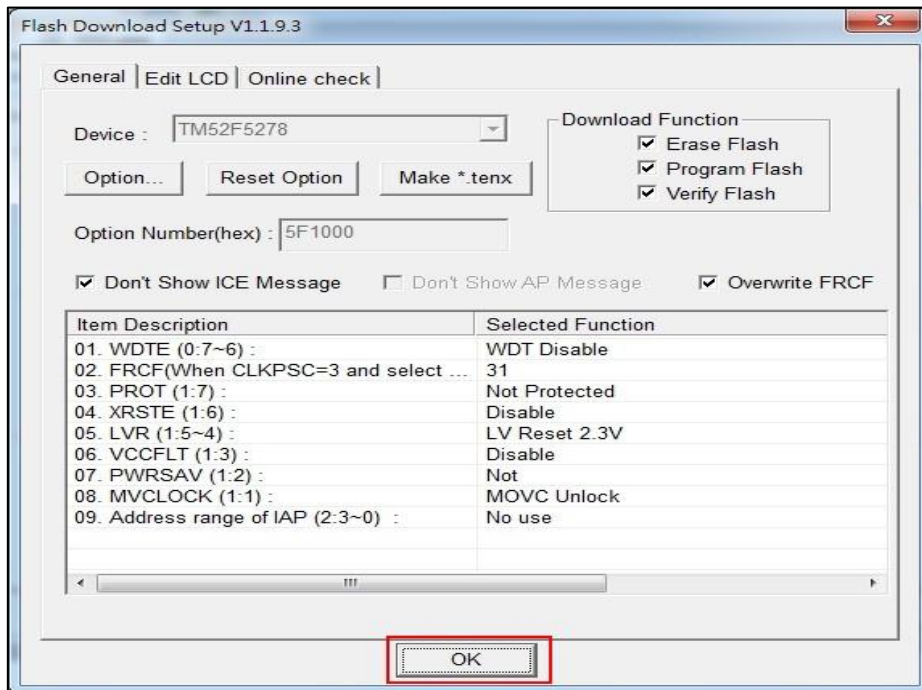
4.16 Click on “Option” button, and the “Smart Option” will be show up, about the option configuration, please refer to TM52F5274/74B/78/78B datasheet.



4.17 FRCF (Fast Internal RC) settings: Click on “Option” button after the “Overwrite FRCF” is checked, the user can adjustment 32 frequency levels (The default is 7.3 Mhz).



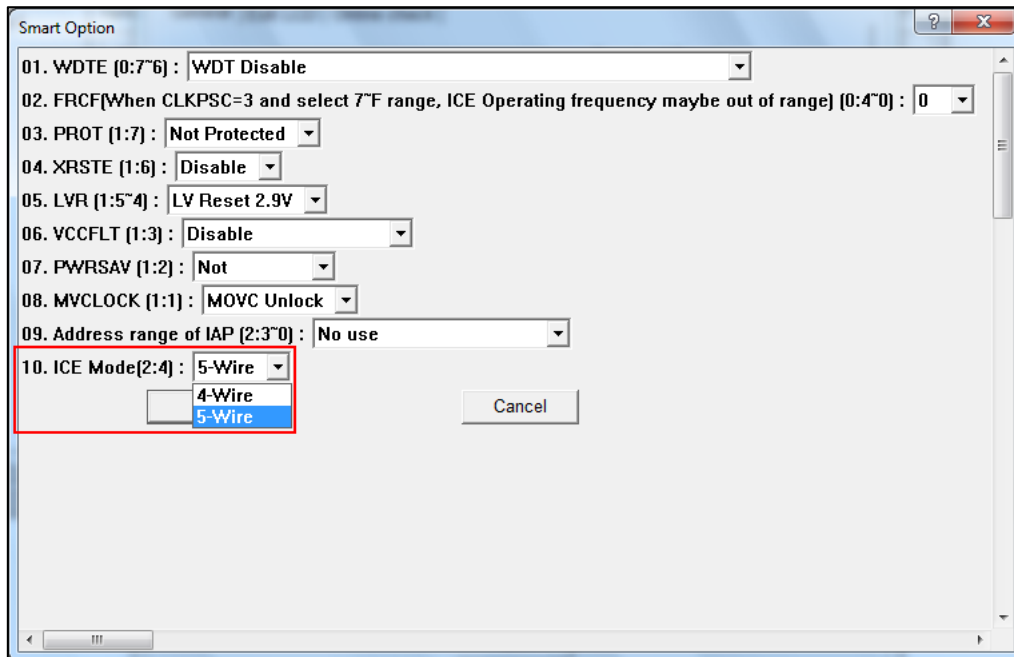
4.18 Click on “OK” to return to “Flash Download Setup” window, and then click on “OK” to return to “Utilities” window, this all new project configuration is complete and click on “OK” to exit the “Option for Target” window, the user can start programming now. (Note: To change chip model, user needs to confirm “Code Range” and “Option” settings, the “Open Project” will save the settings and the “New Project” will be default settings in the “Option” dialog box)



5. How to use four wires to program or simulate by T-Link Board

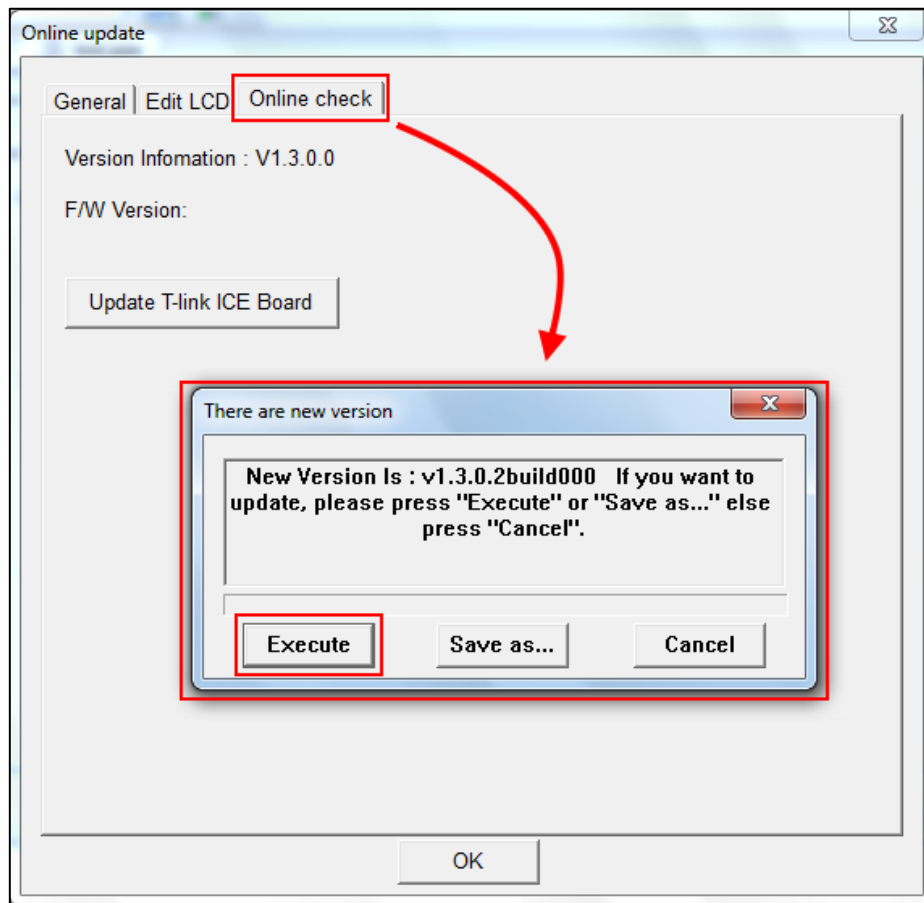
The T-Link ICE Board can use four wires(VCC, VSS, P3.0/P2.4, P3.1/P2.5) or five wires (VCC, VSS, P3.0/P2.4, P3.1/P2.5, P3.3) to program, user must to select 4-wire in “Utilities dialog box settings”.

5.1 Utilities dialog box settings: Click on “Option” button, and select 4-Wire, as shown below.

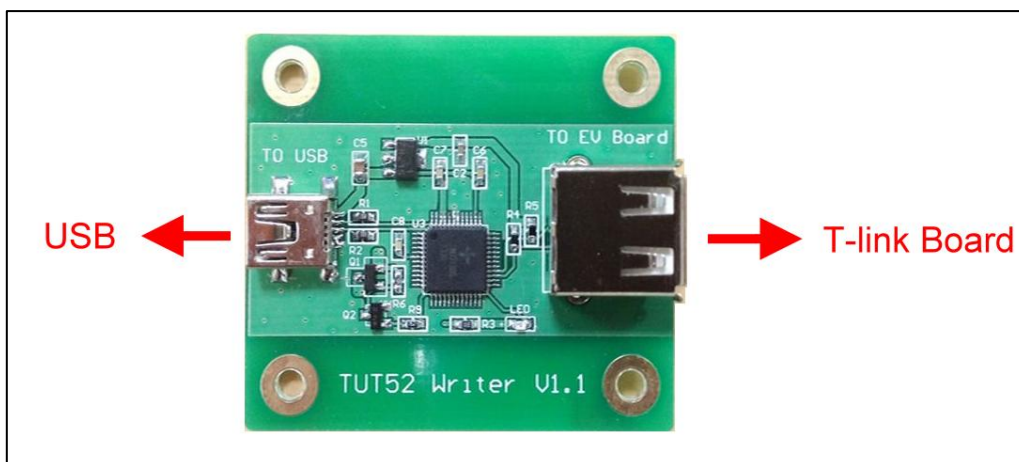


6. How to update tenx IDE & T-Link F/W

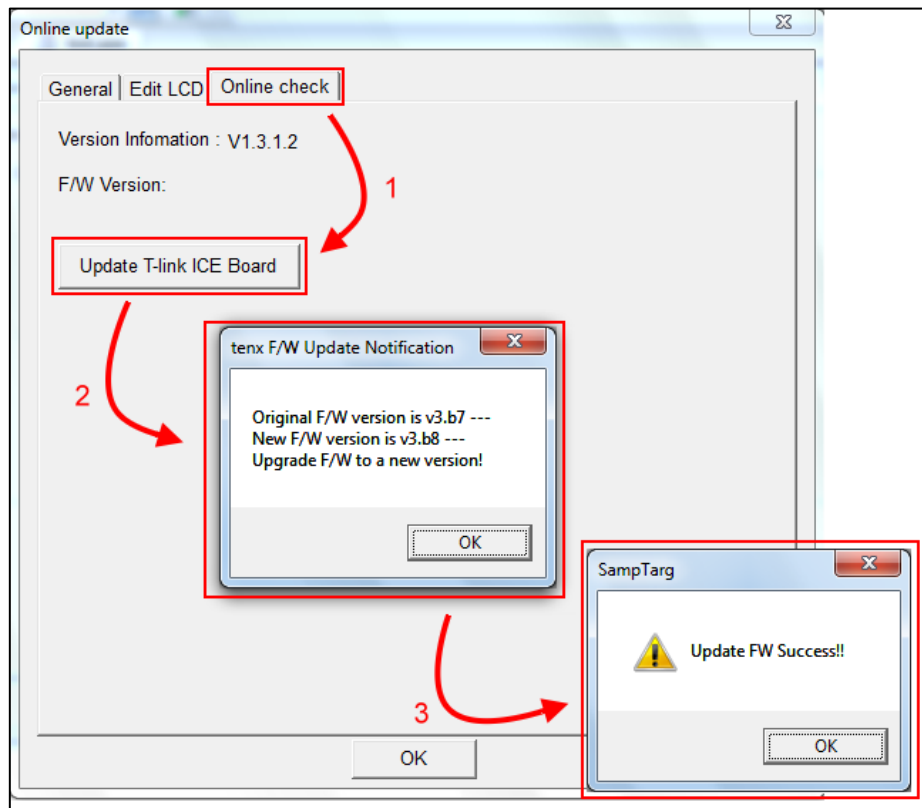
6.1 Update tenx IDE: Click on “Online check” button, and Update the TM52DI1 IDE in Utilities dialog box, as shown below.



6.2 Update T-Link F/W: Connect TUT52 Writer to USB and T-Link Board, as shown below.



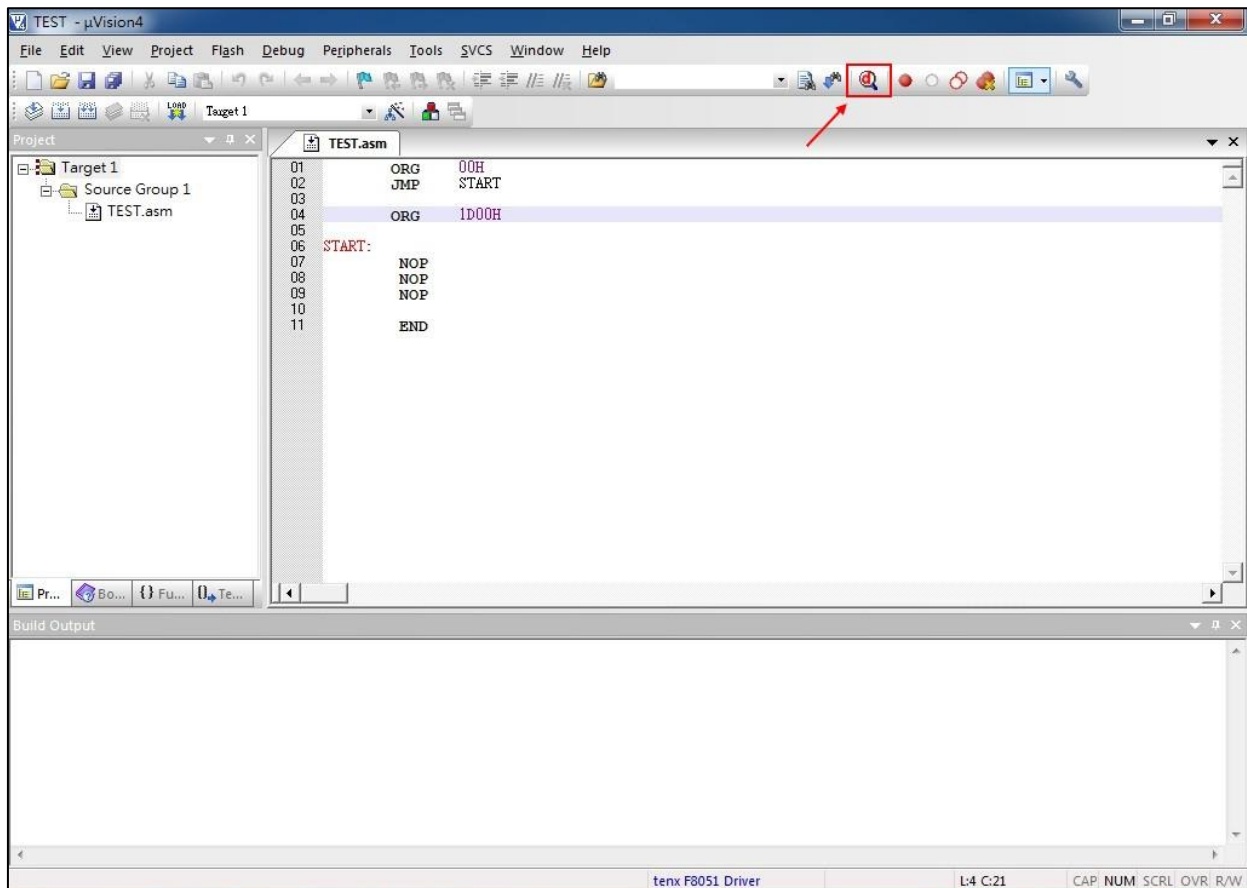
6.3 Click on “Update T-Link ICE Board” button to update T-Link Board F/W, as shown below.



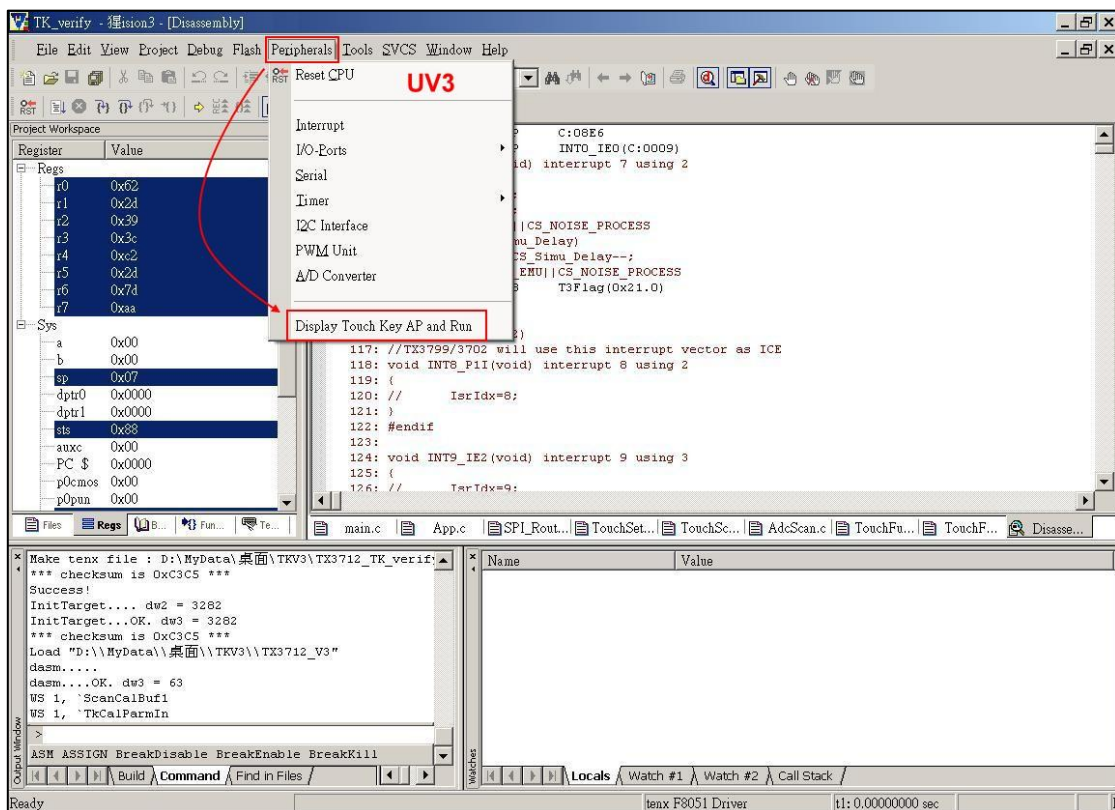
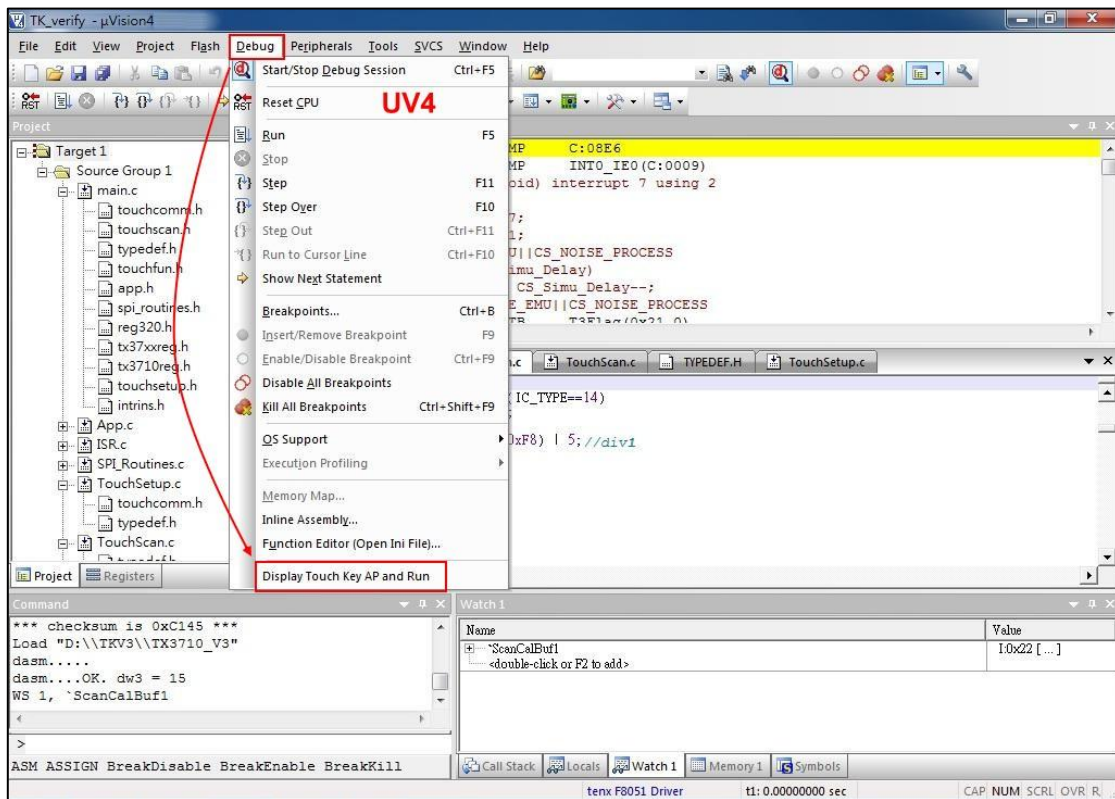
7. How to use Touch Key Application

The Touch Key Application work in Debug Mode, user can use application to view the results of TK Data. The TK Data must be stored in XRAM and IRAM range, as shown below.

7.1 Click on “Debug” into Debug mode.



7.2 Click the Debug menu and select “Display Touch Key AP and Run”.



7.3 Fill in TK Data, ex: TK name (address) and TK Bits and Total TK Channels.

Key-in symbol name for watch

TK Name or Address (Ex: "i:0x20" ; "x:0xFF00") :

TK data bits(8 or 11) : Total TK channels : 0x

Example1:
 TK data address = i:0x22
 TK data bits = 8
 Total TK channels = 0x3

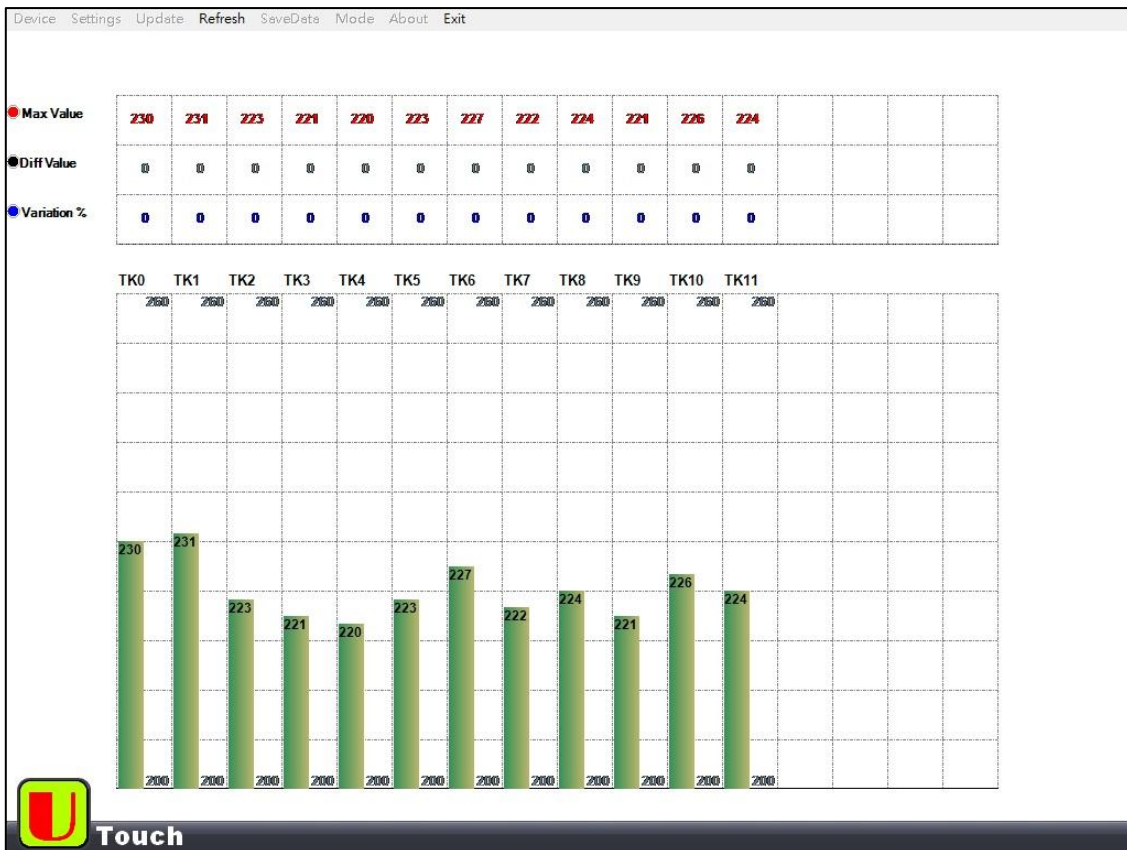
User must:
 Copy {TKDL[7:0]} of 1st TK into i:0x22
 Copy {TKDL[7:0]} of 2nd TK into i:0x23
 Copy {TKDL[7:0]} of 3rd TK into i:0x24

Example2:
 TK data address = i:0x22
 TK data bits = 11
 Total TK channels : 0x2

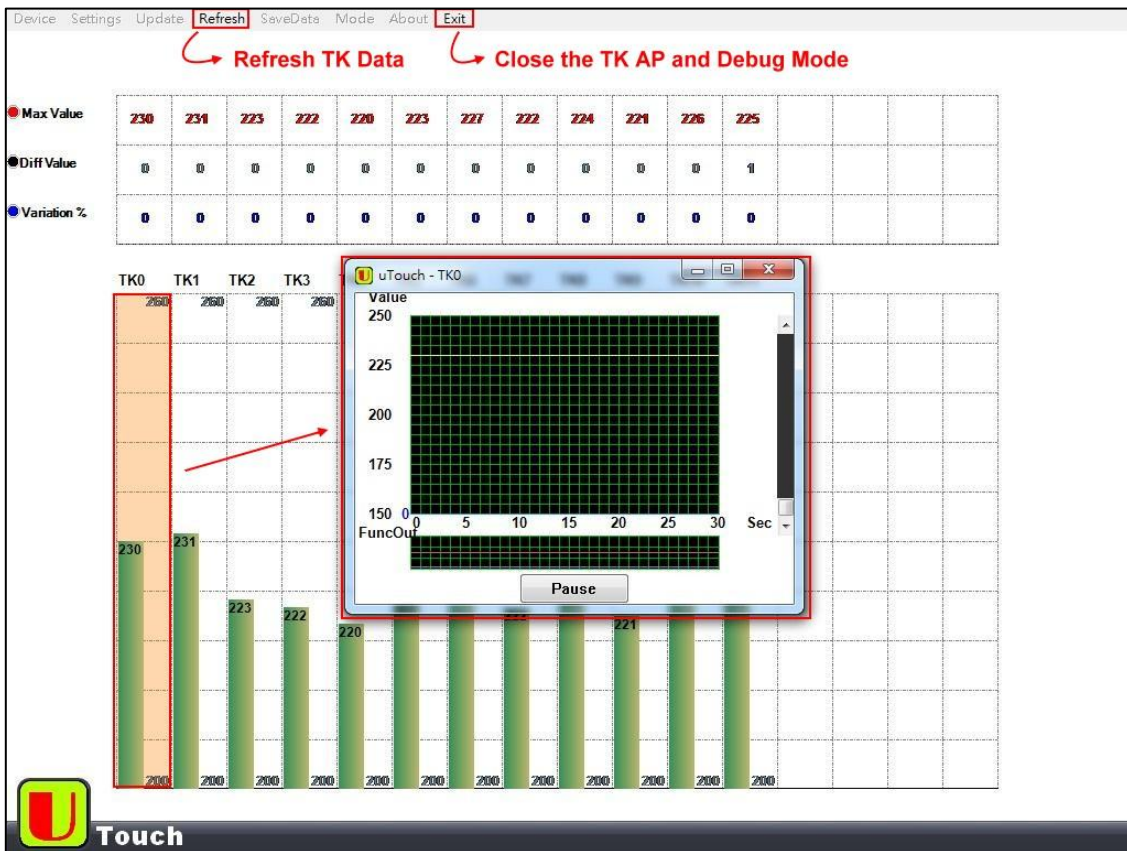
User must:
 Copy {5'b00000, TKOVF, TKDH} of 1st TK into i:0x22
 Copy {TKDL[7:0]} of 1st TK into i:0x23
 Copy {5'b00000, TKOVF, TKDH} of 2nd TK into i:0x24
 Copy {TKDL[7:0]} of 2nd TK into i:0x25

Note:
 1. Any touch key channel on ICE pins(maybe P1.2/P1.3) cannot be enabled.
 2. Press OK, user code will auto freerun and ignore any break point.

Cancel
OK

7.4 Click on “OK” button, and the “TK AP” will be show up.


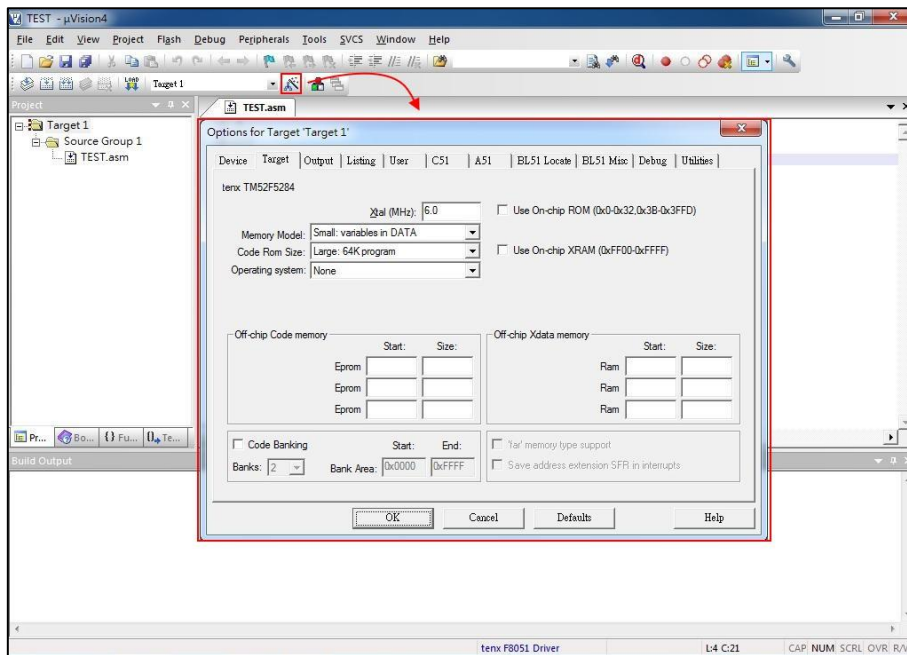
7.5 TK AP Function, as shown below.



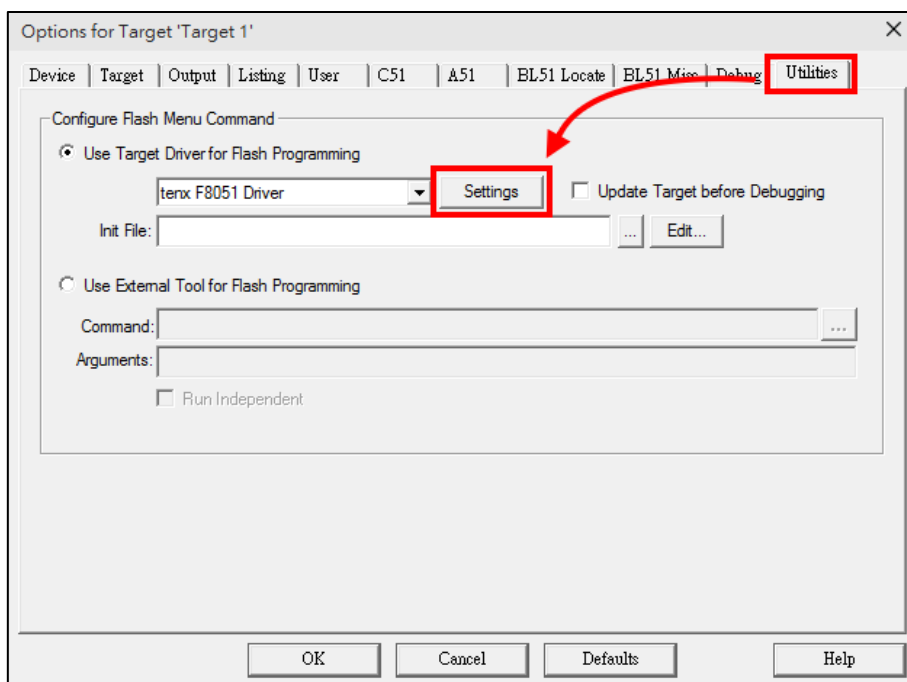
8. How to use LCD Application

The LCD Application can edit and work in Debug Mode, user can use application to simulator LCD Module, please refer to UM-EV22_52XX_LCDAP_SV090 User Manual for more information.

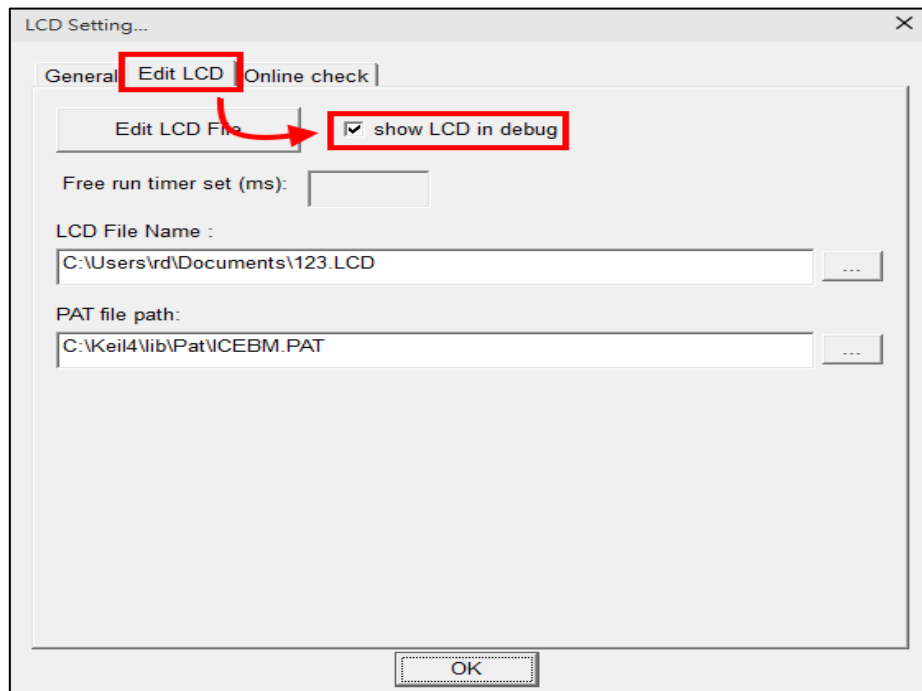
8.1 Click on “Option for Target” button in the main menu, as shown below.



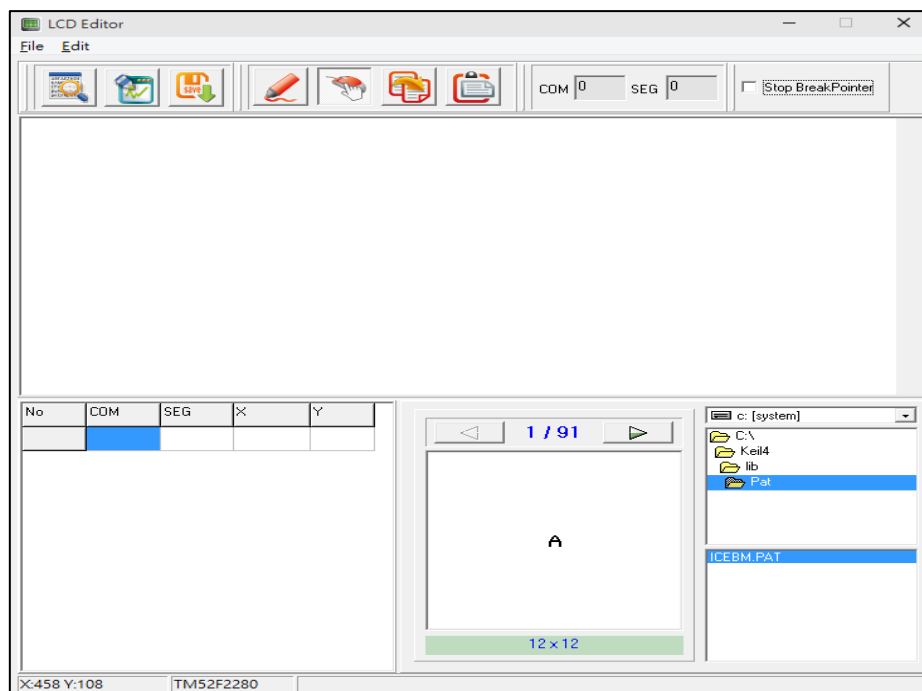
8.2 Click on “setting” Button in “Utilities” Digital box, as shown below.



8.3 Click on LCD “Edit Button” and check “show LCD in debug” option, as shown below.



8.4 Click on “Debug” into Debug mode and the LCD Application will be show up, as shown below.



9. LVR Setting Notes

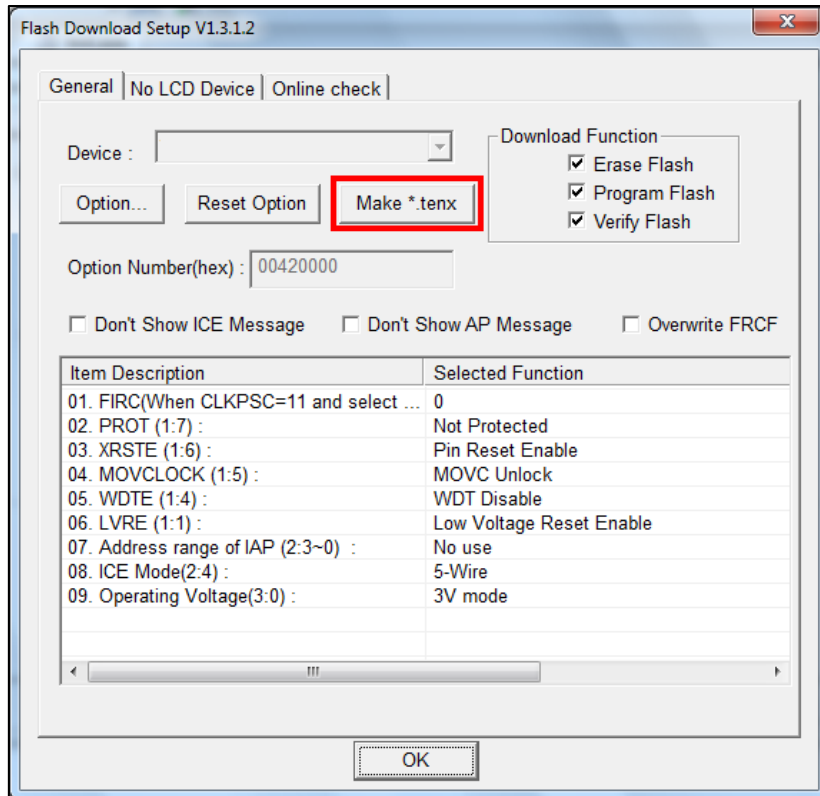
| System clock | | LVR Options (Minimum) | | | | | |
|--------------|-----------|----------------------------------|------------------|----------------------------------|--------------------------------------|----------------------------------|--------------------------------------|
| Source | Frequency | M5254 M5258 F5284 F5288 | F5284B F5288B | F5264 F5268 F5274 F5278 | F5264B F5268B F5274B F5278B | F2280 F2284 F2230 F2234 | F2280B F2284B F2230B F2234B |
| FXT/2 | 8 MHz | / | 2.9V | 2.9V | 2.9V | / | 2.9V |
| FRC/1 | 7.3 MHz | | | | | | 2.8V |
| FXT/2 | 6 MHz | 2.9V | 2.9V | 2.9V | 2.9V | 2.6V | 2.6V |
| FXT/2 | 4 MHz | 2.3V | 2.3V | 2.3V | 2.3V | 2.4V | 2.4V |
| FRC/2 | 3.7 MHz | | | | | | 2.4V |
| FXT/2, /4 | 2 MHz | 1.9V | 1.9V | 1.8V | 1.8V | 2.4V | 2.4V |
| FRC/4 | 1.8 MHz | | | | | | 2.4V |
| FXT/2, /4 | 1 MHz | 1.9V | 1.9V | 1.8V | 1.8V | 1.5V | 1.5V |
| FRC/8 | 0.9 MHz | / | / | / | / | | |

Note: Please refer to TM52XXXX_02SV11 AP Note for more information.

10. Q & A

Q1: How to program a user file on TWR98 Writer ?

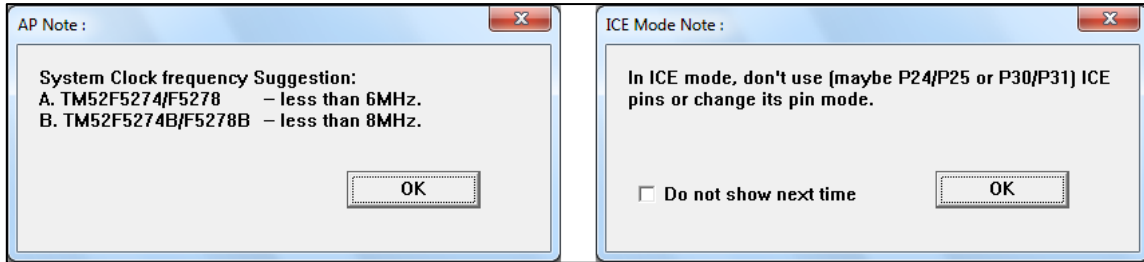
A1: The User must make “*.tenx” file to program in “Utilities dialog box settings”, or enter “Download Mode” and “Debug Mode”, as shown below.



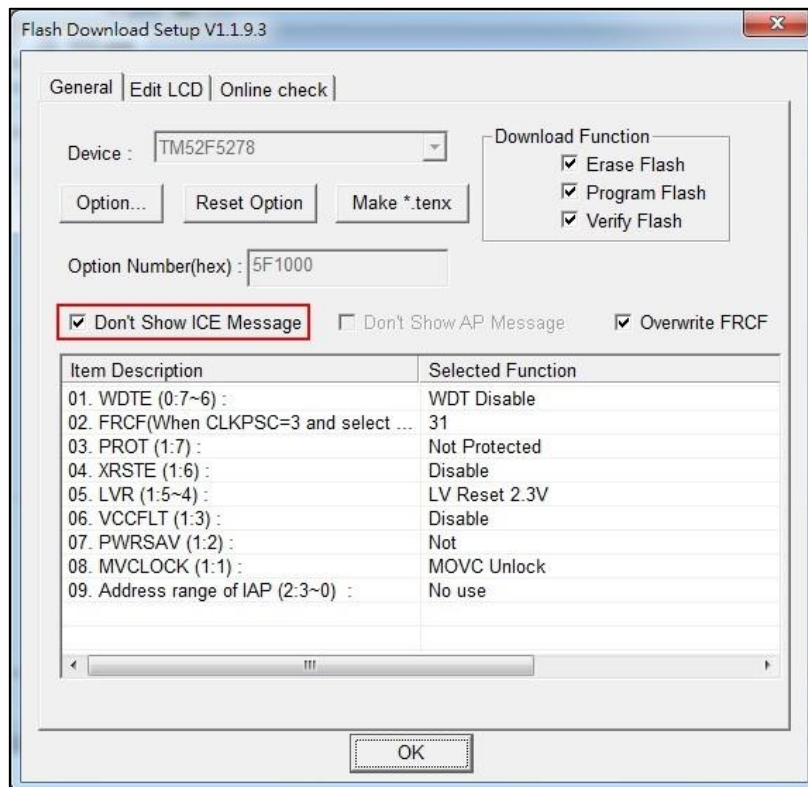
Q2: How to use UV3 and UV4 version in the same time ?

A2: If user wants to install UV3 and UV4 version in the default path (UV3 & UV4 version in C:\ Keil) in the same time, the user musts to change C51 folder name (because there will be two C51 folders), and then install tenx F51 & L51 IDE file, if user needs to use UV3 version, the UV4 version must to change C51 folder name. However, the user wants to install UV3 and UV4 version in the different path (UV3 version in C:\ Keil, UV4 version in D:\ Keil), the tenx F51 & L51 IDE & Keil C version (UV3 or UV4) must be installed in the same path.

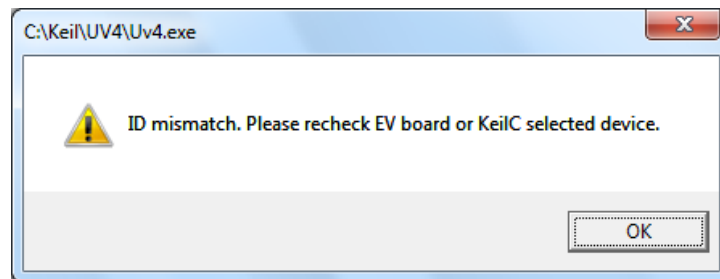
Q3: Why the user clicks on “Debug” button as shown below window ?



A3: When the user writes program file must be avoid to control P3.0/P2.4、P3.1/P2.5 pin (For example : P3、P2 Mode configuration change), if do not show this window in the next time, please check “Do not show next time” option, or to confirm “Don’t Show ICE Message” is checked in the “Flash Download Setup” window.

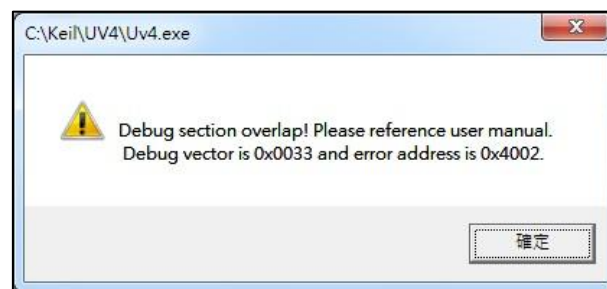


Q4: Why the user clicks on “Debug” button as shown below window, and then exit the “Debug Mode” ?



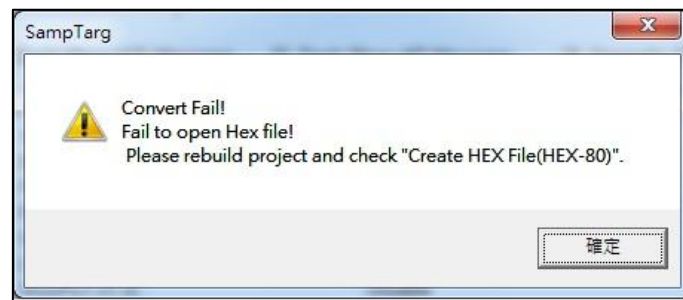
A4: Because the user selects chip model and EV Board model does not match, please recheck chip model and EV Board model.

Q5: Why the user clicks on “Debug” button or “Download” button as shown below window, and then exit the “Debug Mode” or “Download Mode” ?



A5: Because the user writes program file is out of “ROM code” range, please refer Note (2) in Page 14.

Q6: Why the user clicks on “Debug” button as shown below window, and then exit the “Debug Mode” ?



A6: Because the “Create HEX File” is not checked in “Output” option, please refer below figure.

